

[illegible]

# 01

The diagram consists of a central horizontal bar with a light blue background. On the left side of this bar is a dark blue vertical bar. A row of small blue squares is positioned within the light blue bar, starting from the left edge. Above and below the light blue bar are rows of small gray squares. The top row of gray squares is shorter than the bottom row. The row of blue squares is shorter than the bottom row of gray squares.

# 02

Diagram illustrating the structure of a 24-bit instruction:

The instruction is divided into four fields:

- Op (Operation): 4 bits
- Rs (Register Source): 5 bits
- Rt (Register Target): 5 bits
- Im (Immediate): 10 bits

The instruction format is shown as a sequence of bits: 000000000000000000000000.

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED] PEST [REDACTED] SWOT [REDACTED] STP [REDACTED]

[REDACTED]

[REDACTED]

# 03

[REDACTED] MVP [REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]